

**Advanced Metallization And Interconnect Systems For
ULSI Applications In 1995 By Russell & Shi-Qing Wang
(eds.) Ellwanger**

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Patent us5942799 - multilayer diffusion barriers -

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Patent us5674787 - selective electroless copper

Patent US5674787 - Selective electroless copper deposited

Thermal stability of ultra-thin CoSi_2 films on

the thermal stability of ultra-thin in: R.C. Ellwanger and Shi-Qing Wang (Eds.), Advanced Metallization and Interconnect Systems for ULSI Applications

Reliability of advanced metallization systems

Reliability of advanced metallization systems. Rosa Leon, Jet Propulsion Laboratory March 2003. Brief description of advanced interconnect technology .

Ultrathin diffusion barriers/liners for gigascale

Advanced Metallization and Interconnect Systems for ULSI Applications in 1996, Advanced Metallization and Interconnect Systems for ULSI Applications in 1995,

Ieee international interconnect technology

IEEE International Interconnect Technology Conference/ Advanced will be held in conjunction with the Advanced Metallization and interconnect systems.

Metallization - ieee conferences, publications,

2010 Materials for Advanced Metallization novelties in alternative interconnect systems and more widely advanced materials and structures relevant to micro and

Preview: conference looks beyond 32-nm to 3-d | ee

The 2011 Advanced Metallization Conference will be devoted to leading-edge research in the field of advanced metallization and 3-D integration for ULSI IC applications.

Ieee xplore abstract - advanced interconnect

Advanced interconnect systems for ULSI advanced interconnect systems of low-k dielectric and dual damascene architecture for both Al and Cu metallization

Patent us5695810 - use of cobalt tungsten

A technique for electrolessly depositing a CoWP barrier material on to copper and electrolessly depositing copper onto a CoWP barrier material to prevent copper

Cinii books - wang, shi- qing

Advanced metallization and interconnect systems for ULSI applications in 1995 : Russell C. Ellwanger, Shi-Qing Wang.

Sub-half micron metallization using high-pressure

Sub-half micron metallization using high Plugs, Advanced Metallization and Interconnect Systems for Advanced Metallization and Interconnect Systems

Event details: materials for advanced

Materials for advanced metallization. novelties in alternative interconnect systems and more widely advanced materials and structures * Cu interconnect

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Method for low temperature annealing of

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Venue: Proc. Advanced Metallization and Interconnect Systems for ULSI Applications

Advanced metallization and interconnect systems

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Patent us7144805 - method of submicron

Methods for depositing a metal into a micro-recessed structure in the surface of a microelectronic workpiece are disclosed. The methods are suitable for use in

Temperature dependence of decomposition pathway of

The decomposition of dimethylaluminum hydride of Advanced Metallization and Interconnect of Advanced Metallization and Interconnect Systems for

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Low dielectric constant materials for advanced

S. Jeng et al., Advanced Metallization and Interconnect Systems for ULSI Applications in 1995, Low dielectric constant materials for advanced interconnects Journal

Patent us7244677 - method for filling recessed

A method for filling recessed micro-structures at a surface of a semiconductor wafer with metallization is set forth. In accordance with the method, a metal layer is

Cinii - advanced metallization and

Advanced metallization and interconnect systems for ULSI applications in Advanced metallization and interconnect systems for Russell C. Ellwanger, Shi-Qing Wang

Patent us6806186 - submicron metallization using

Methods for depositing a metal into a micro-recessed structure in the surface of a microelectronic workpiece are disclosed. The methods are suitable for use in

Patent us7001471 - method and apparatus for

A method for filling recessed microstructures at a surface of a microelectronic workpiece, such as a semiconductor wafer, with metallization is set forth. In

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Patent us6565729 - method for electrochemically

Patent US6565729 - Method for electrochemically depositing

Advanced metallization conference 2003 amc 2003

Cambridge University Press Advanced Metallization Conference 2003 Gary W. Ray, Novellus Systems , Inc., San Jose; Tom S. Smy, Carleton University,

Selective electroless copper deposited

A method or utilizing electroless copper deposition to selectively form encapsulated copper plugs to connect conductive regions on a semiconductor. A via opening in

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This conference is devoted to leading-edge research in the field of advanced metallization and 3-D the-art and future directions in interconnect systems.

Method for fabrication of a semiconductor device

A method for fabricating a device, the method including: providing a first layer including first transistors, where the first transistors include a mono-crystalline

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